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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,808	11/19/2001	Manfred Bartz	CYPR-CD01169M	1806

7590 04/14/2005
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EXAMINER

WHITMORE, STACY

ART UNIT PAPER NUMBER

2825

DATE MAILED: 04/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

SM

Office Action Summary	Application No. 09/989,808	Applicant(s) BARTZ ET AL.	
	Examiner Stacy A. Whitmore	Art Unit 2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 19 November 2001.
- 2a) This action is **FINAL**.
- 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-35 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-35 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 19 November 2001 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
 * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____
- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: _____

DETAILED ACTION

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the subject matter disclosed, e.g., in claims 1, 13, and 22, and others must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

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(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

2. Claims 1-5, 11-16, 19, 21, 22-25, and 30-35 are rejected under 35 U.S.C. 102(a) as being anticipated by Cypress Microsystems, "PsoC Designer: Integrated development environment, getting started 25 – minute tutorial", Version 1.0".

3. As for claims 1-5, 11-16, 19, 21, 22-25, and 30-35, Cypress Microsystems discloses the invention as claimed, including

4. As for claims 1, 13, and 22, Elayda discloses the invention as claimed, including the method computer readable medium storing instructions, and system for: receiving inputs to configure said circuit, said circuit implementing a plurality of modules [pg. 10]; and generating at least two elements selected from the group consisting of: an application programming interface (API) for programming an operation of a first of said modules, source code for realizing said plurality of modules in hardware, an interrupt vector table having a call to an interrupt service routine for said first of said modules, and a data sheet for said circuit, wherein said circuit comprises said plurality of modules [pg. 14-16].

5. As for claims 2-5, 11-12, 14-16, 19, 21, 23-25, and 30-35, Cypress Microsystems discloses generating at least three elements selected from said group [pgs. 14-16]; wherein b) comprise generating: said API for programming said operation of said first of said modules, said source code for realizing said plurality of modules in said hardware, said interrupt vector table having said call to said interrupt service routine for said first of said modules, and said data sheet for said circuit, wherein said circuit comprises said plurality of modules [pgs. 14-16 and pg. 11]; said inputs to configure said circuit comprise parameters for said modules [pg. 11]; said inputs to configure said circuit comprise parameters for said

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modules and placements for said modules in a graphical user interface describing resources operable to implement said modules [pg. 7-11];conditionally compiling said API based on said inputs to configure said circuit [pg. 16]; said source code is derived from the placement of said modules in a graphical user interface that describes resources operable to implement said modules and parameters of said modules, said parameters being inputs to configure said circuit [pg. 7, 15]; said source code is derived from parameters of said modules, said parameters being inputs to configure said circuit [pg. 7, 15]; conditionally compiling a first of said APIs based on said inputs to configure said circuit [pg. 15];said application programming interfaces comprise a function call, said call operable to cause a first module of said modules to perform a predetermined function [pg. 14-16];said application programming interfaces comprise an interrupt service routine for a first of said modules [pg. 14];wherein b) comprises automatically inserting, in said interrupt vector table, a call to said interrupt service routine, said interrupt service routine servicing a module placed in said graphical user interface, wherein said call is inserted in said interrupt vector table based on inputs to configure said circuit [pg. 14-16].

6. Claims 1, 10, 13, 20, 22, and 29 are rejected under 35 U.S.C. 102(a) as being anticipated by Cypress Microsystems, "PsoC technology complete changes 8-bit MCU system design" (hereinafter referred to as Cypress 2).

7. As for claims 1, 13, and 22, Cypress 2 discloses the invention as claimed, including the method computer readable medium storing instructions, and system for: receiving inputs to configure said circuit, said circuit implementing a plurality of modules [pg. 1, 4, 8-10]; and generating at least two elements selected from the group consisting of: an application programming interface (API) for programming an operation of a first of said modules, source code for realizing said plurality of modules in hardware, an interrupt vector table having a call to an interrupt service routine for said first of said

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modules, and a data sheet for said circuit, wherein said circuit comprises said plurality of modules [pg. 1, 4, 8-10].

8. As for claims 10, 20, and 29, Cypress 2 further discloses source code operable to program a microcontroller to implement said modules [pgs. 1, 4, 10-12,

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

9. Claims 6-9, 17-18, and 26-28 are rejected under 35 U.S.C. 103(a) as being obvious over Cypress Microsystems, "PsoC technology complete changes 8-bit MCU system design" (hereinafter referred to as Cypress 2) in view of Weiss (US Patent 6,711,731).

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art only under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 103(a) might be overcome by: (1) a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not an invention "by another"; (2) a showing of a date of invention for the claimed subject matter of the application which corresponds to subject matter disclosed but not claimed in the reference, prior to the effective U.S. filing date of the reference under 37 CFR 1.131; or (3) an oath or declaration under 37 CFR 1.130 stating that the application and reference are currently owned by the same party and that the inventor named in the

application is the prior inventor under 35 U.S.C. 104, together with a terminal disclaimer in accordance with 37 CFR 1.321(c). This rejection might also be overcome by showing that the reference is disqualified under 35 U.S.C. 103(c) as prior art in a rejection under 35 U.S.C. 103(a). See MPEP § 706.02(l)(1) and § 706.02(l)(2).

10. As for claims 6-9, 17-18, and 26-29, Cypress 2 discloses the invention substantially as claimed, including [claims 1, 13, and 22] the method, computer readable medium storing instructions, and system for:
receiving inputs to configure said circuit, said circuit implementing a plurality of modules [pg. 1, 4, 8-10]; and
generating at least two elements selected from the group consisting of:
an application programming interface (API) for programming an operation of a first of said modules, source code for realizing said plurality of modules in hardware, an interrupt vector table having a call to an interrupt service routine for said first of said modules, and a data sheet for said circuit, wherein said circuit comprises said plurality of modules [pg. 1, 4, 8-10];
and further discloses module files and device description file describing resources operable to implement said modules [pgs. 1, 4, 8].

Cypress 2 does not specifically disclose HTML and XML files.

Weiss discloses HTML and XML files.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Cypress 2 and Weiss because implementing Cypress 2's data sheet and files in HTML and/or XML would allow for Cypress 2's data sheet and files to be shared between designers over the internet therefore taking advantage of remote resources and improving design effort [see Weiss, col. 5, lines 33-67].

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11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stacy A. Whitmore whose telephone number is (571) 272-1685. The examiner can normally be reached on Monday-Thursday, alternate Friday 6:30am - 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Stacy A Whitmore
Primary Examiner
Art Unit 2825



SAW

April 11, 2005